

Fig. 1

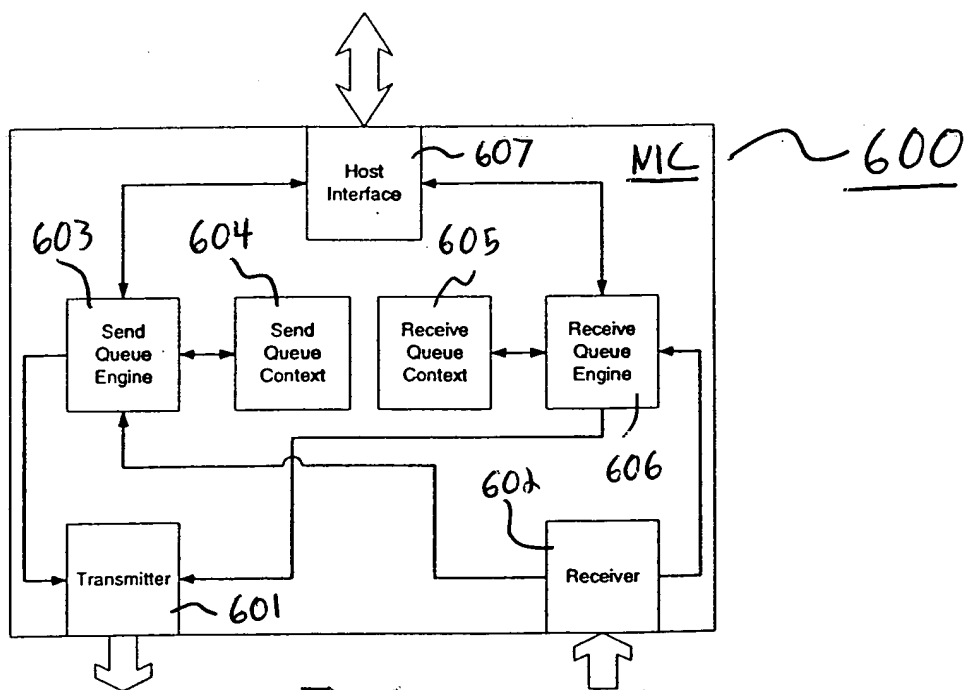


Fig. 6

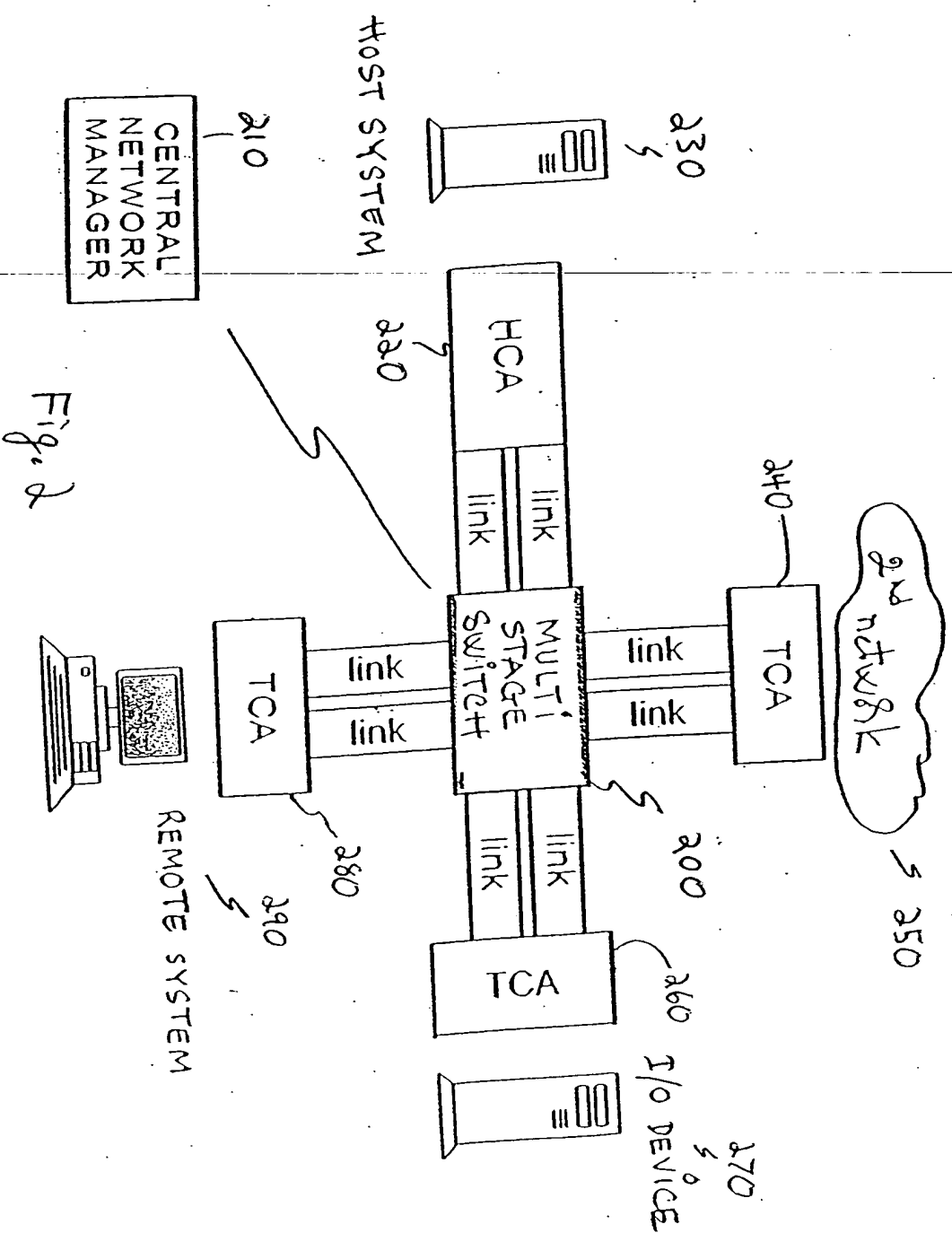


Fig. 2

FIG. 2 is a block diagram of a network architecture in accordance with the present invention. The network architecture includes a host system 230, a remote system 290, a central network manager 210, a multi-stage switch 200, and a second network 250. The host system 230 is connected to the multi-stage switch 200 via a host connection adapter 220. The remote system 290 is connected to the multi-stage switch 200 via a terminal connection adapter 280. The central network manager 210 is connected to the multi-stage switch 200 via a host connection adapter 220. The multi-stage switch 200 is connected to the second network 250 via a terminal connection adapter 240. The multi-stage switch 200 is also connected to an I/O device 270 via a terminal connection adapter 260.

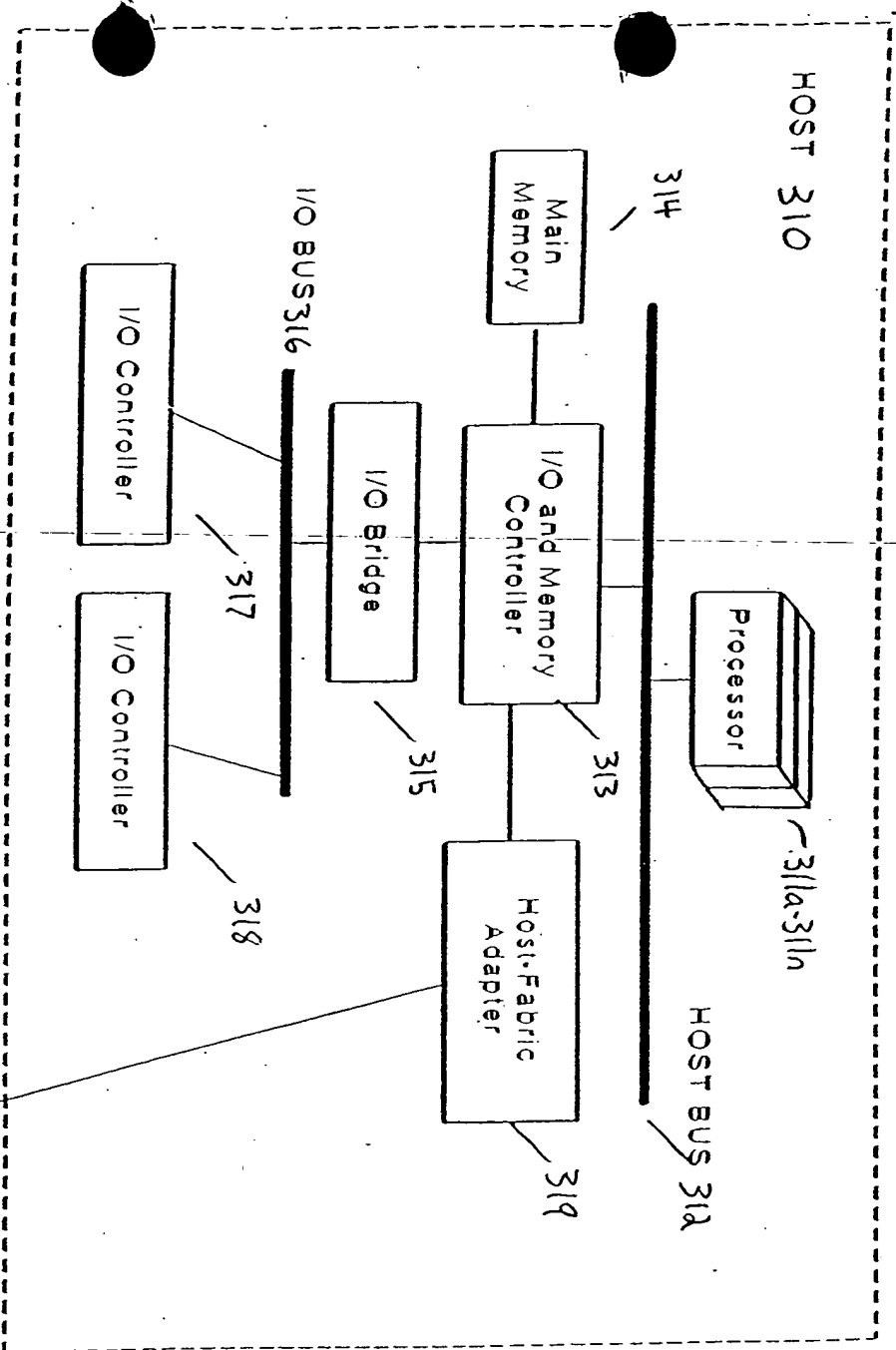
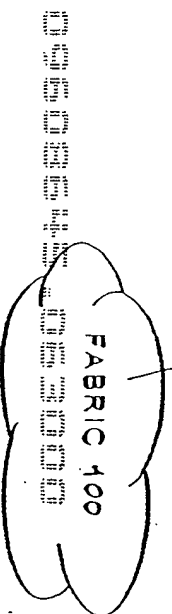


Fig. 3



HOST 310

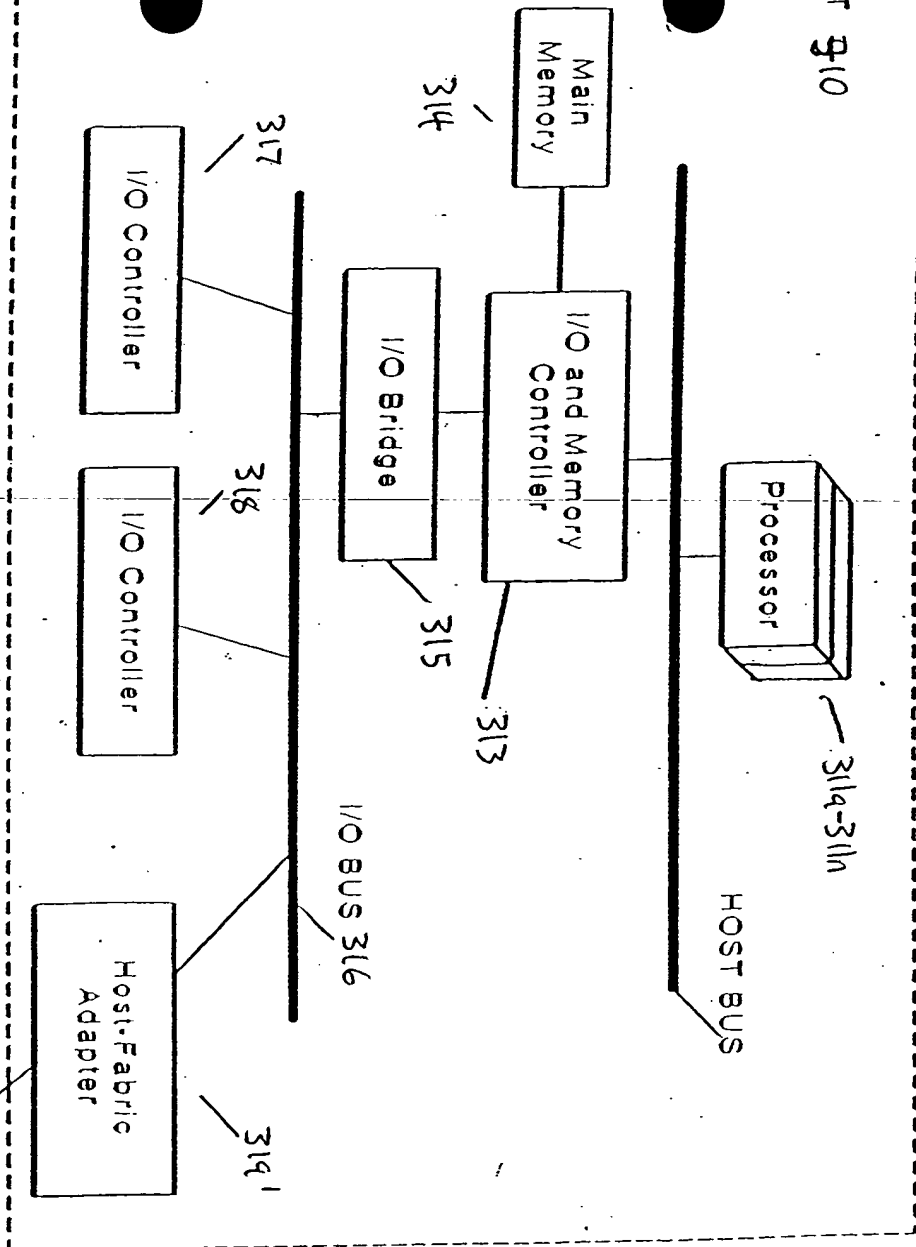


Fig. 4

FIG. 4 is a block diagram of a host system 310, which is connected to a fabric 100. The host system 310 includes a processor 313, main memory 314, and an I/O and memory controller 315. The I/O and memory controller 315 is connected to an I/O bridge 316, which is in turn connected to an I/O bus 317. The I/O bus 317 is connected to two I/O controllers 318 and 319. The I/O controller 319 is connected to a host-fabric adapter 320, which is connected to the fabric 100.

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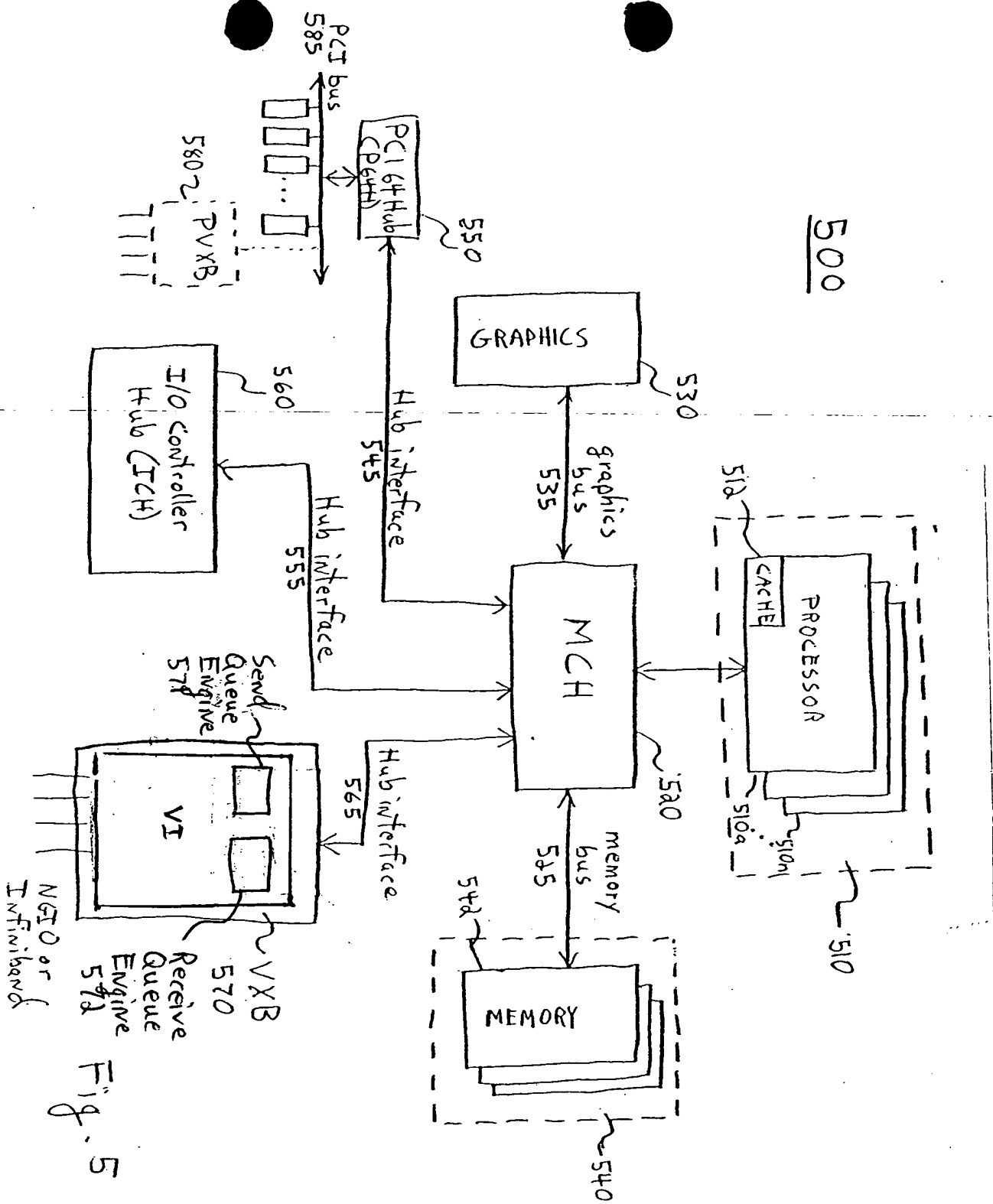


Fig. 5



